

22-141 50 SHEETS
22-142 100 SHEETS
22-144 200 SHEETS



AMERICAN
22-141 50 SHEETS
22-142 100 SHEETS
22-144 200 SHEETS

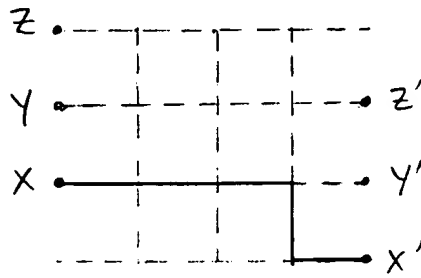


FIGURE 1

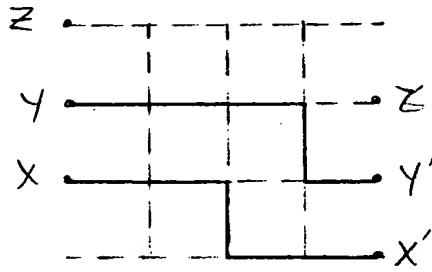


FIGURE 2

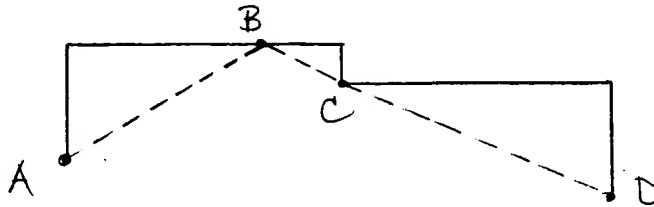


FIGURE 3

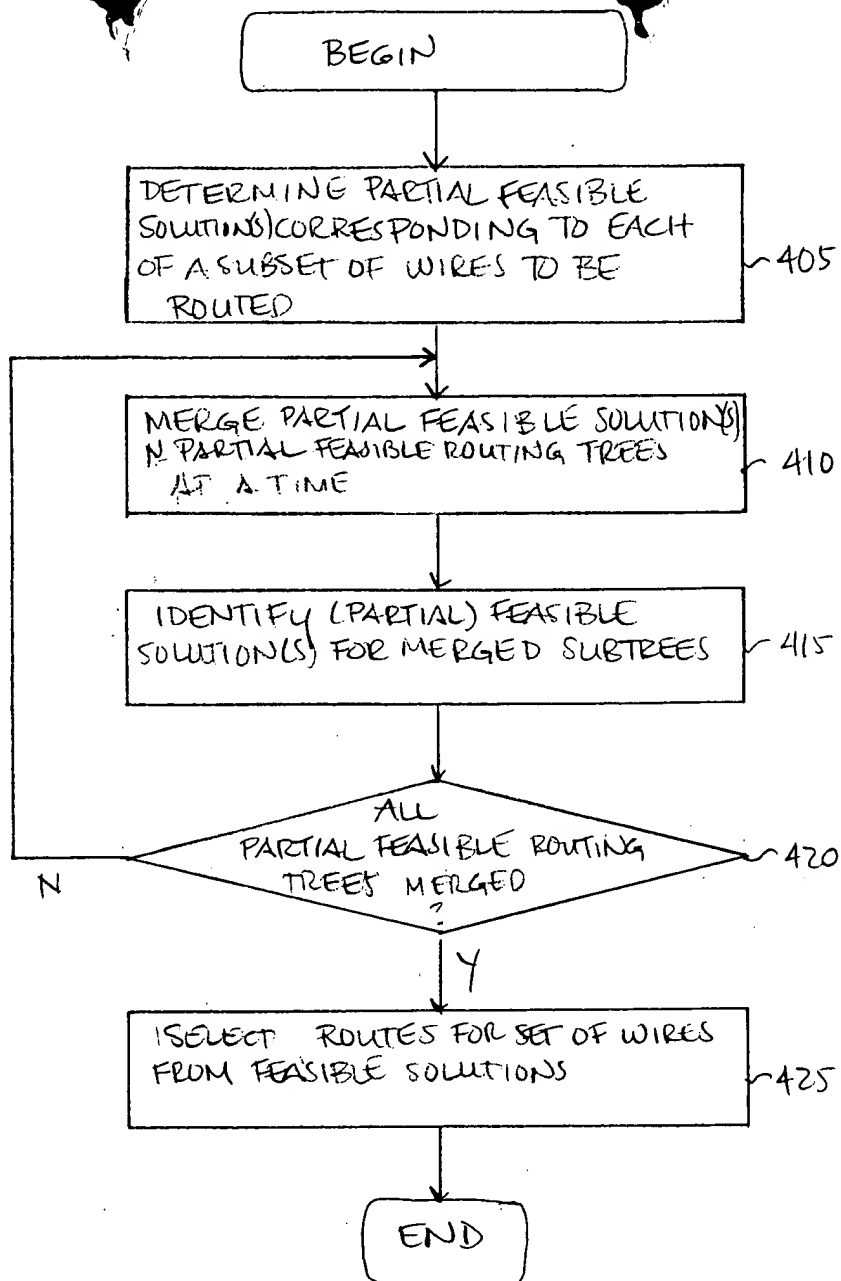
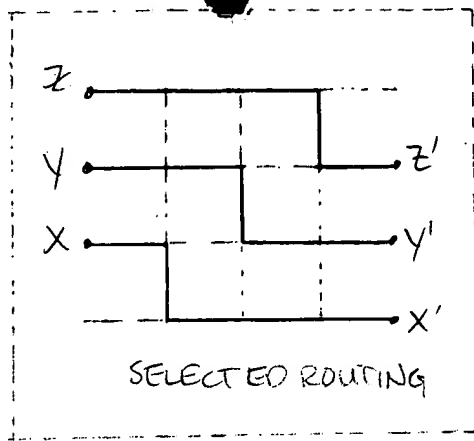


FIGURE 4



$XX' \& YY' \& ZZ'$

$XX'(3) \& YY'(2) \& ZZ'(1)$ 512

FEASIBLE SOLN(S)

$XX' \& YY'$

$XX'(2) \& YY'(1)$
(1)
 $XX'(3) \& YY'(1)$
(2)
 $XX'(3) \& YY'(2)$
(3)

510

PARTIAL FEASIBLE SOLNS $XX' \& YY'$

XX'

YY'

ZZ'

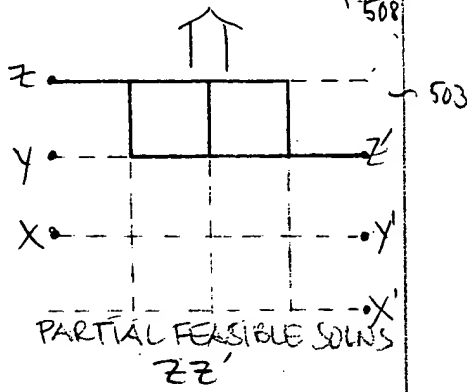
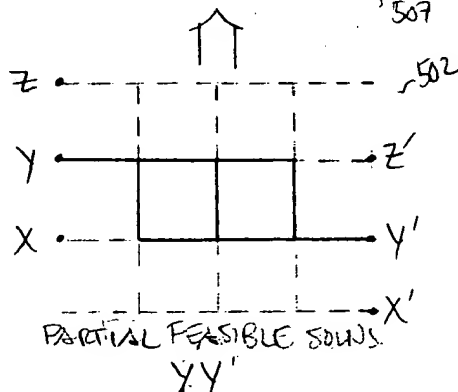
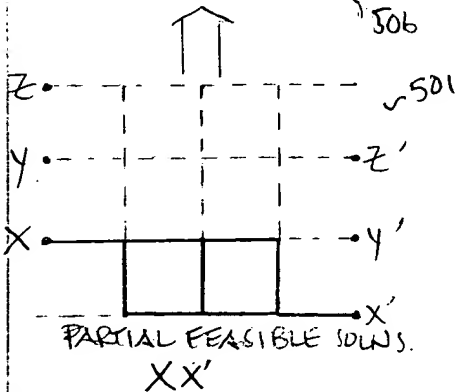
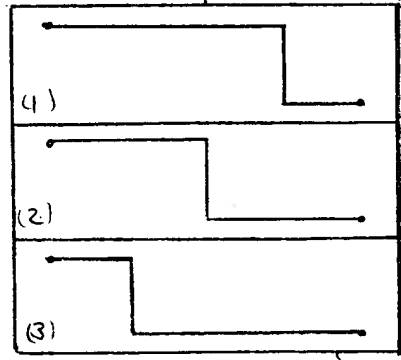
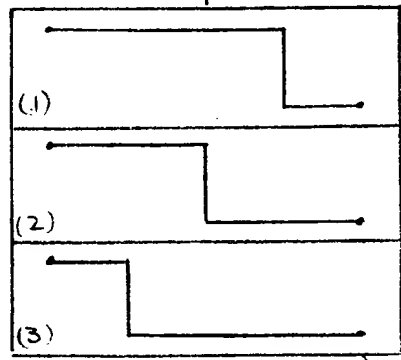
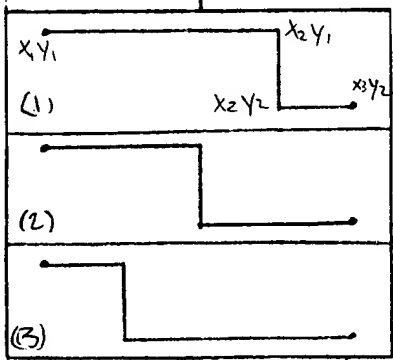


FIGURE 5

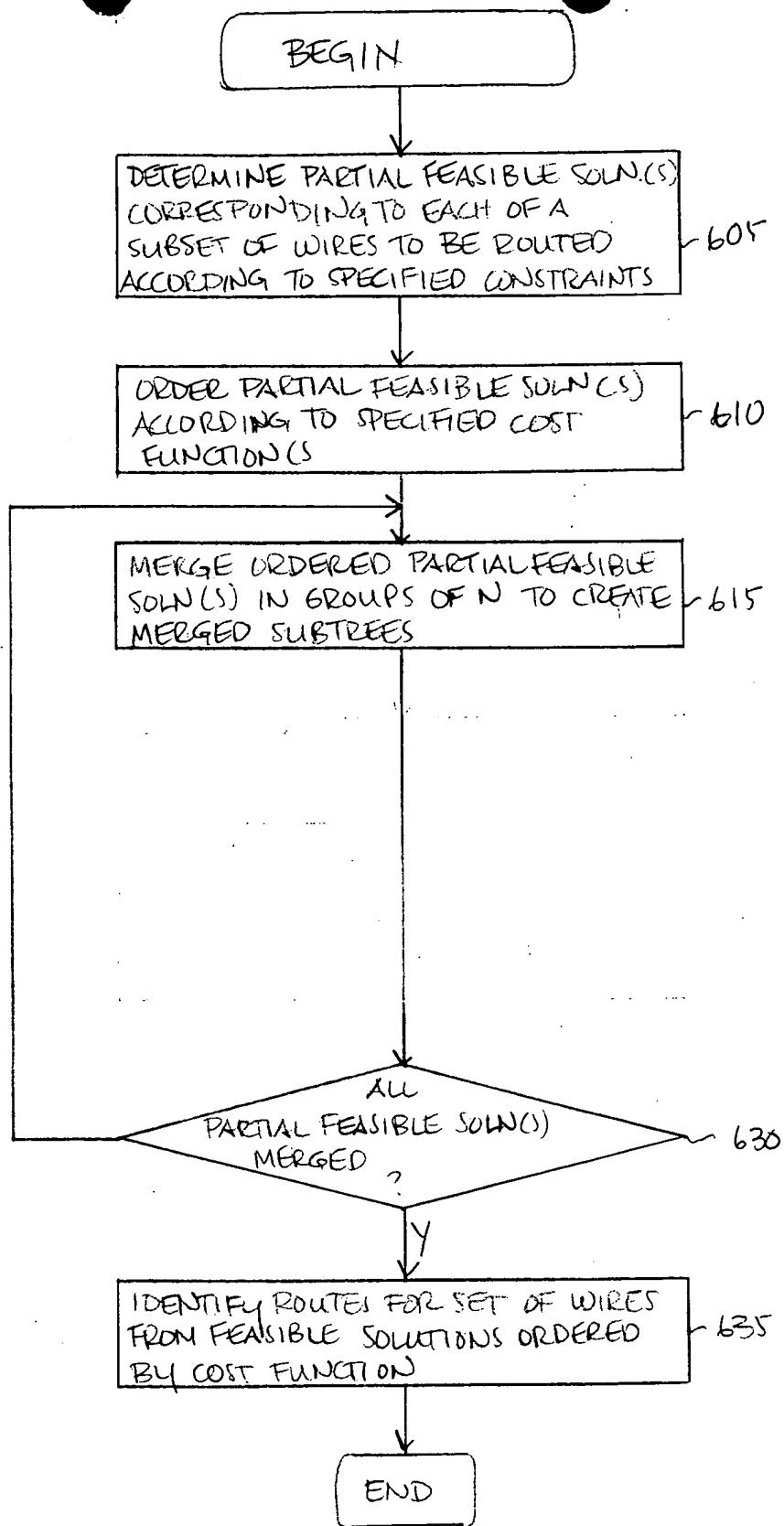
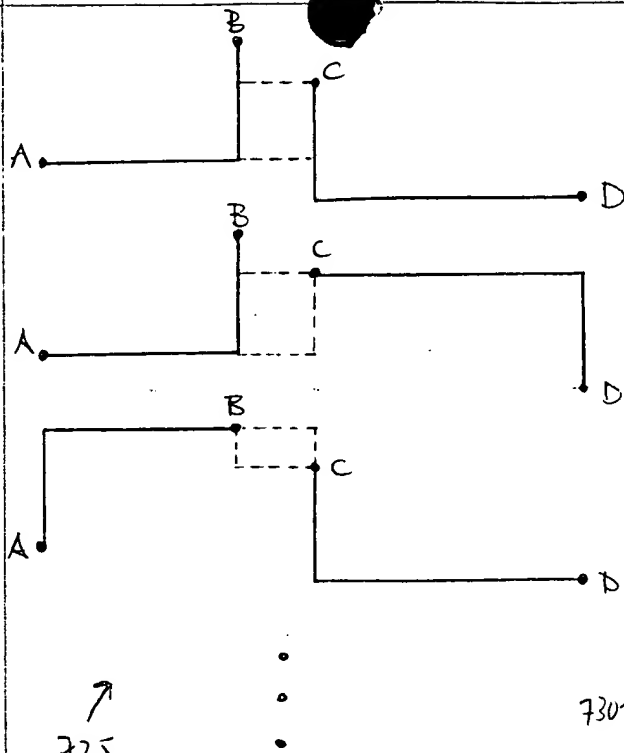
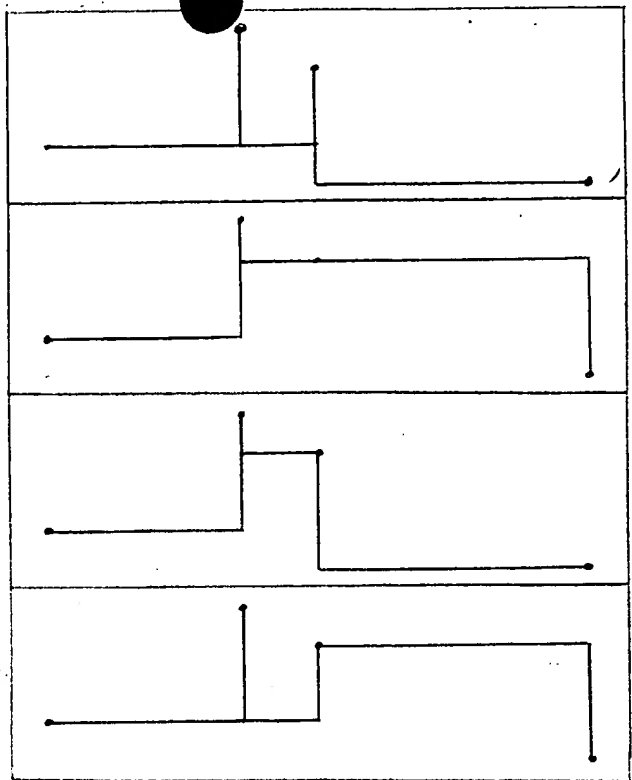


FIGURE 6



725

730



FEASIBLE SOLNS ABCD

SOLNS = 2

HEAP DEPTH = 4

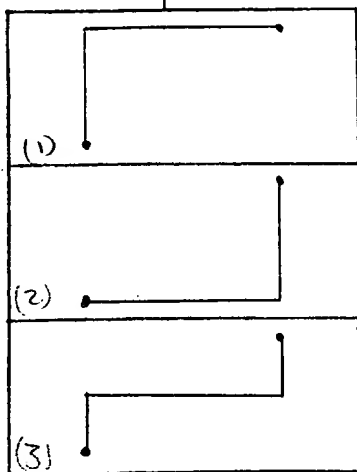
SORTED BY:

- 1) WIRE LENGTH
- 2) SOURCE TO SINK DELAY

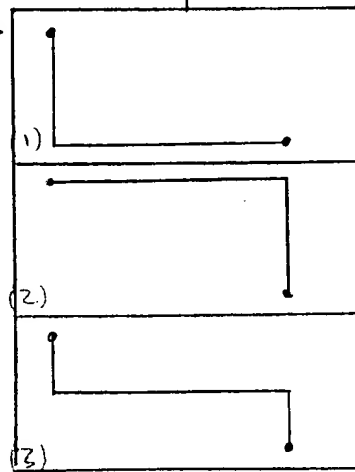
AB

CD

LOWEST
COST. SOLN.



~710



~720

SOLNS = 3

HEAP DEPTH = 3

SORTED BY:

- 1) WIRE LENGTH
- 2) SOURCE TO SINK DELAY

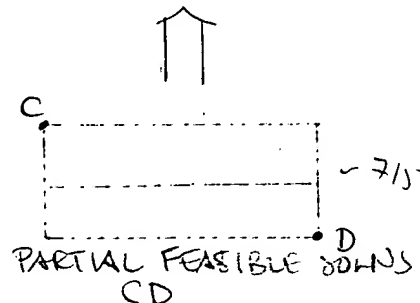
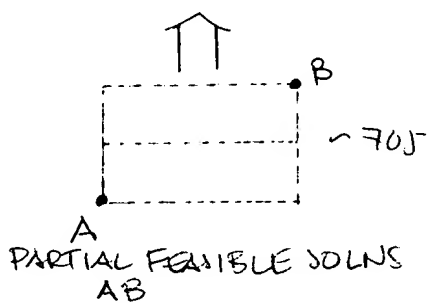


FIGURE 7

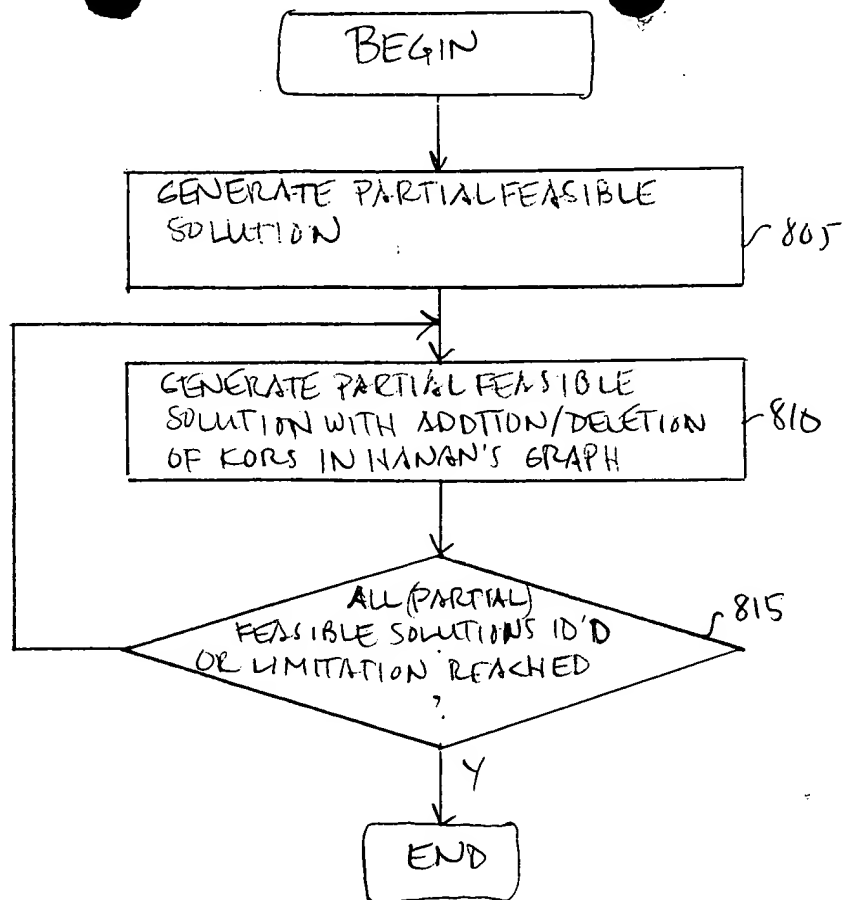


FIGURE 8

22-141 50 SHEETS
22-142 100 SHEETS
22-144 200 SHEETS



AMERICAN
INSTITUTION
OF
SCIENCE
AND
TECHNOLOGY
LIBRARY
1000
PENN
AVENUE
N.W.
WASHINGTON
D.C. 20004

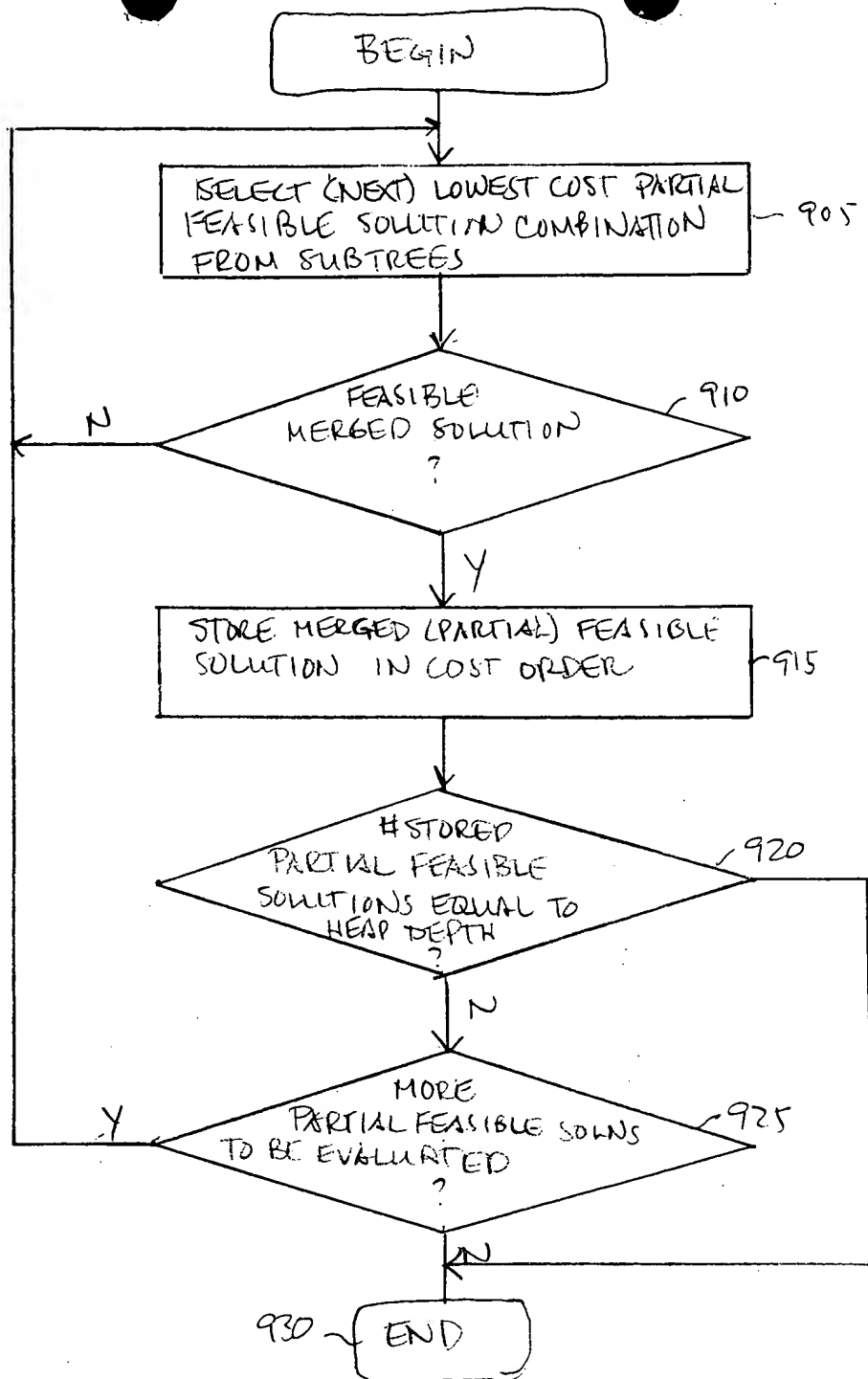


FIGURE 9



SA AUTOMATED
ROUTING ENGINE
DESIGNER'S TOOL
FOR PCB LAYOUT
SYSTEM 1000

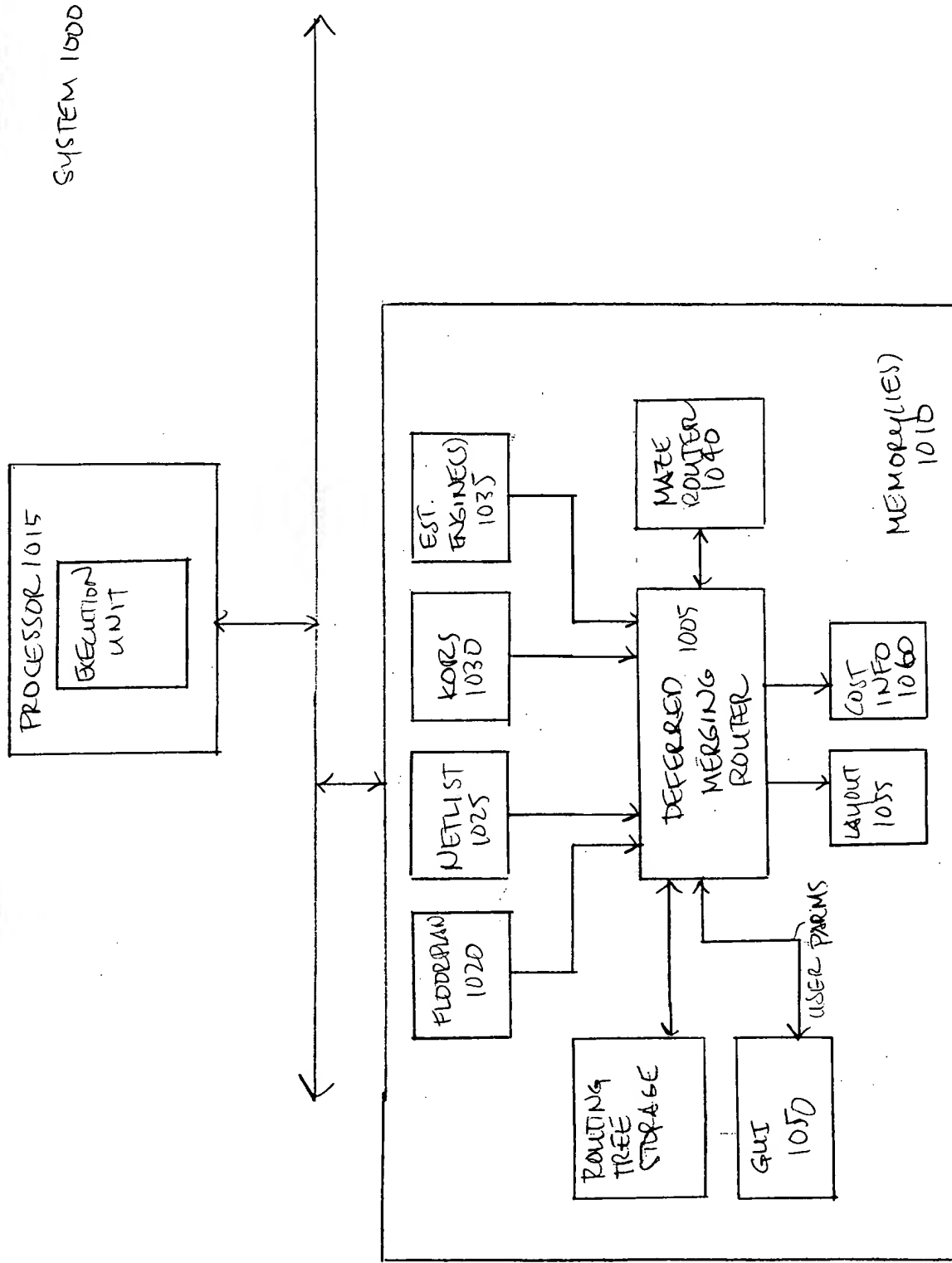


FIGURE 10